

Electronic Patent Application Fee Transmittal

Application Number:	10016449
Filing Date:	10-Dec-2001
Title of Invention:	Method and system for use of a field programmable gate array (FPGA) function within an application specific integrated circuit (ASIC) to enable creation of a debugger client within the ASIC
First Named Inventor:	Robert Thomas Ballis
Filer:	Kelvin M. Vivian/Jinny Nguyen
Attorney Docket Number:	RPS920010127US1

Filed as Large Entity

Utility Filing Fees

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
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Basic Filing:

Pages:

Claims:

Miscellaneous-Filing:

Petition:

Patent-Appeals-and-Interference:

Filing a brief in support of an appeal	1402	1	500	500
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Post-Allowance-and-Post-Issuance:

Extension-of-Time:

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Miscellaneous:				
Total in USD (\$)				500